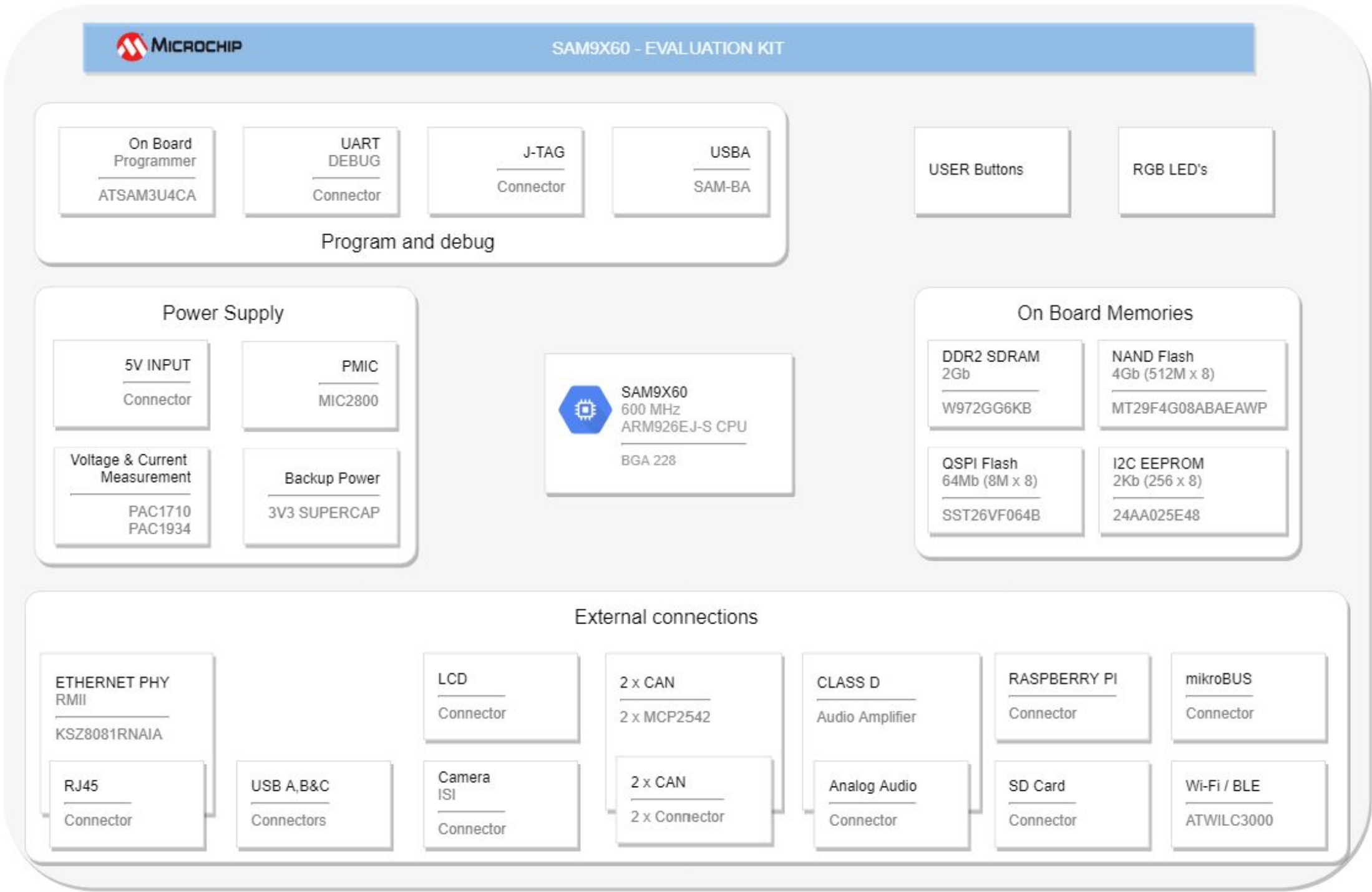
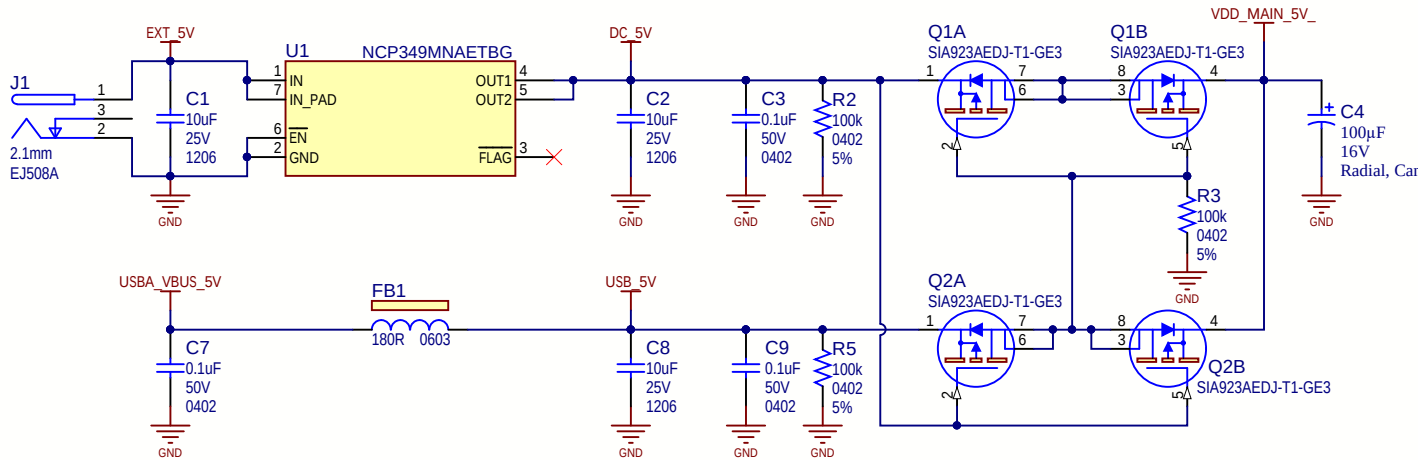


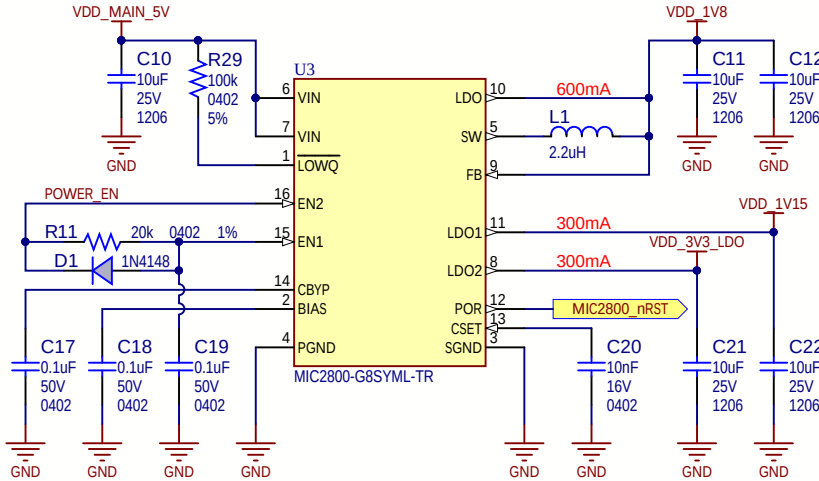


Drawn By: IGN		 MICROCHIP	
Engineer: IGN			
PartNumber: DT100126		Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title Block Diagram		Variant Name Standard_WILC3000_DNP	Designed with  Altium.com
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:44 AM	
	Revision:2	Sheet 1 of 13	
File: 01 - Block Diagram.SchDoc			

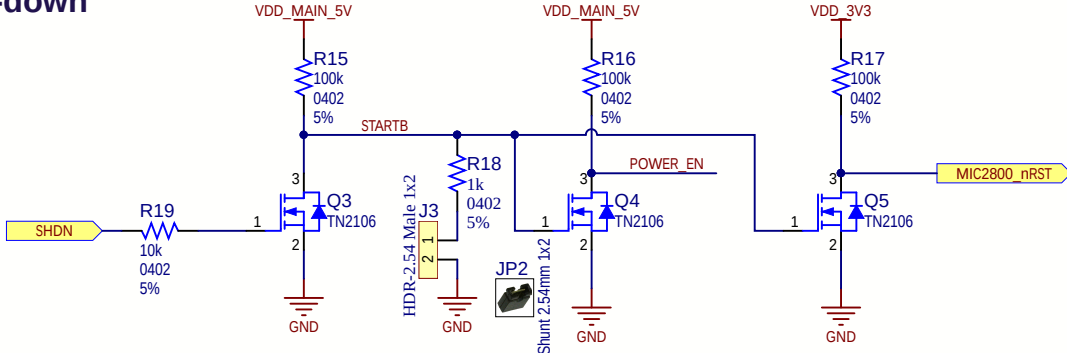
Input Power Options



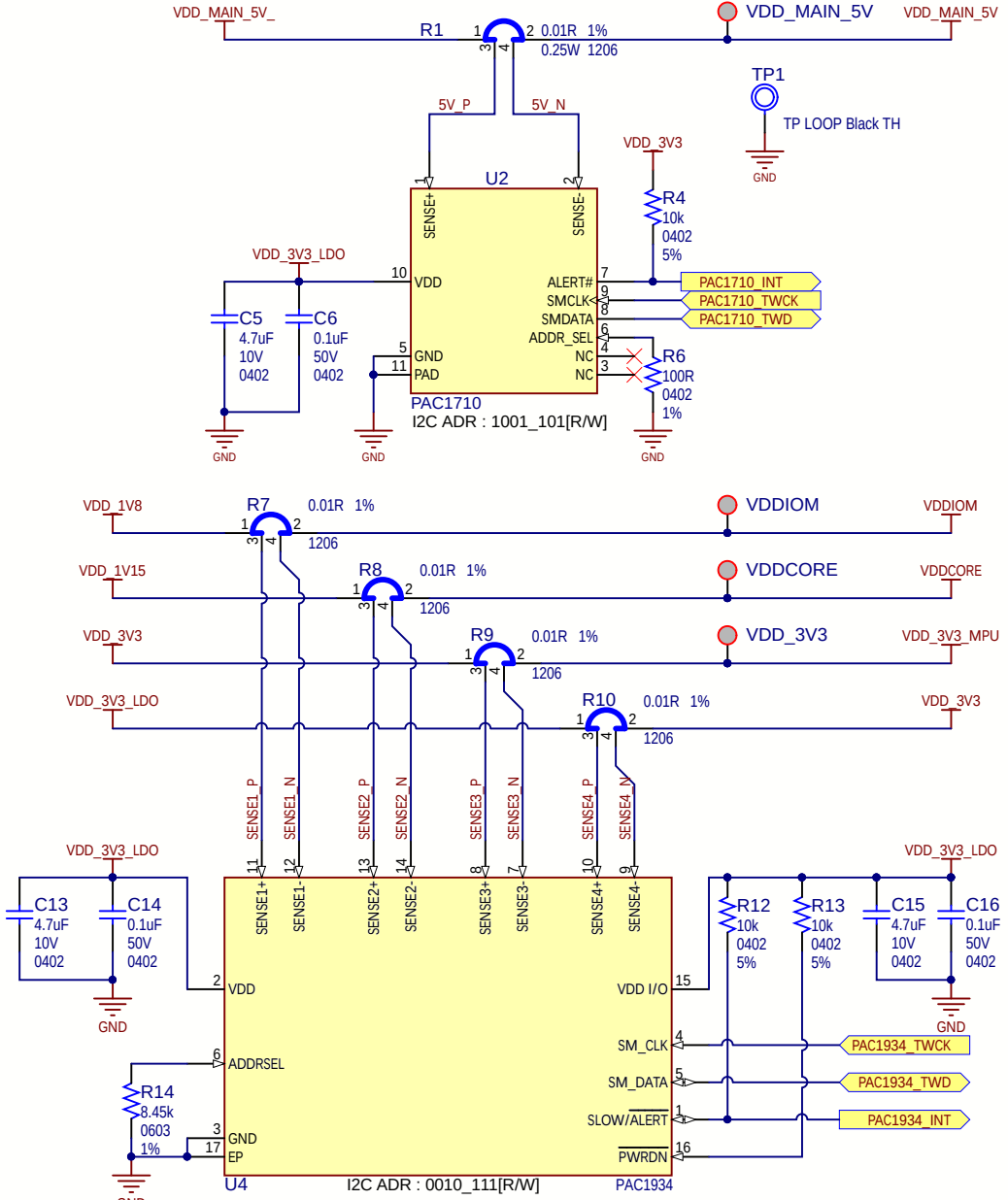
PMIC



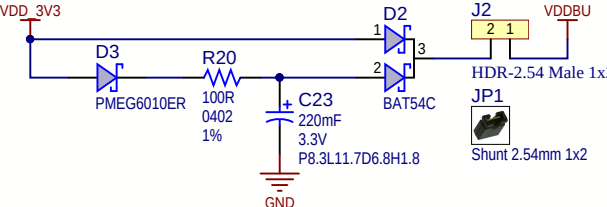
Shut-down



Power Measurement

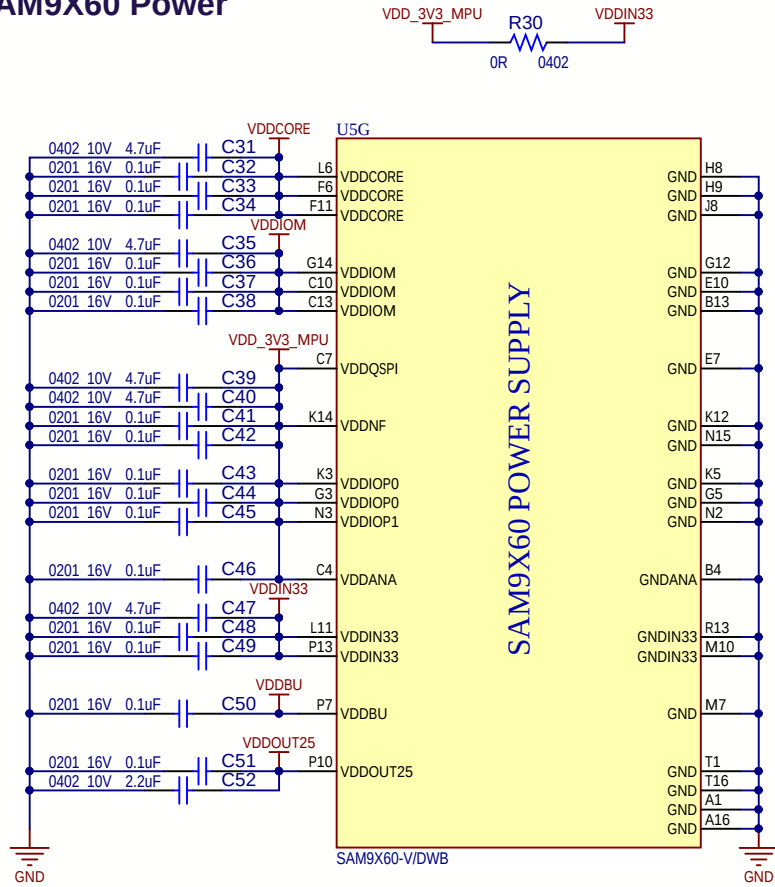


Battery Unit



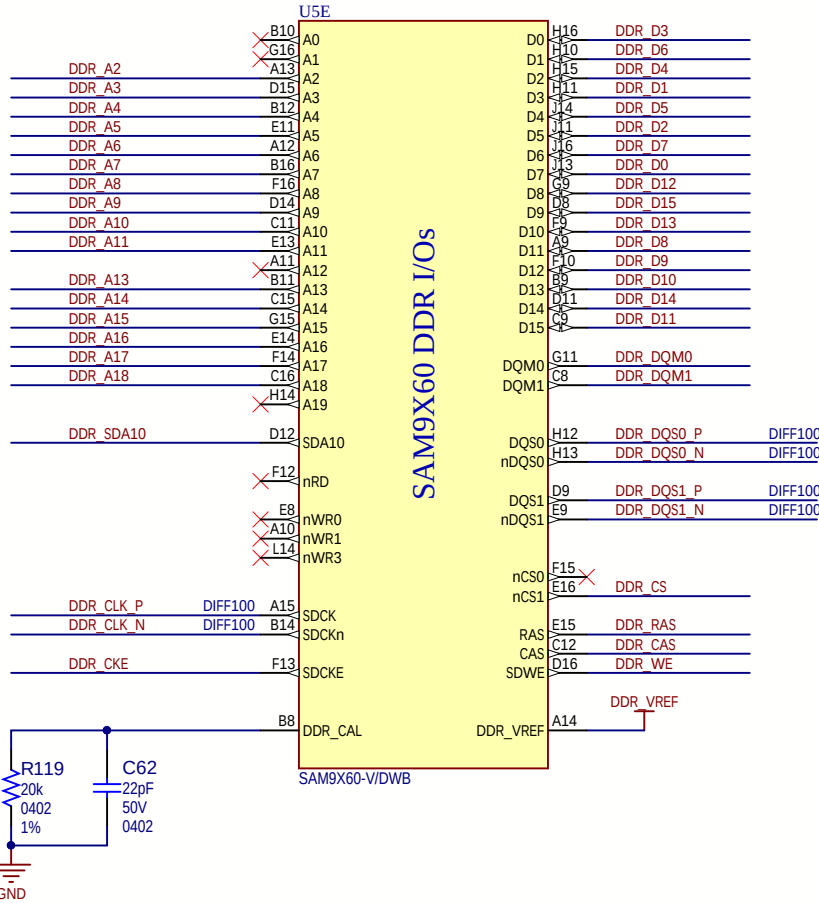
Drawn By: IGn	 MICROCHIP	
Engineer: IGn		
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit	
Sheet Title Power supply	Variant Name Standard_WILC3000_DNP	Designed with  Altium
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:44 AM
	Revision:2	Sheet 2 of 13
File: 02 - Power supply.SchDoc		

SAM9X60 Power

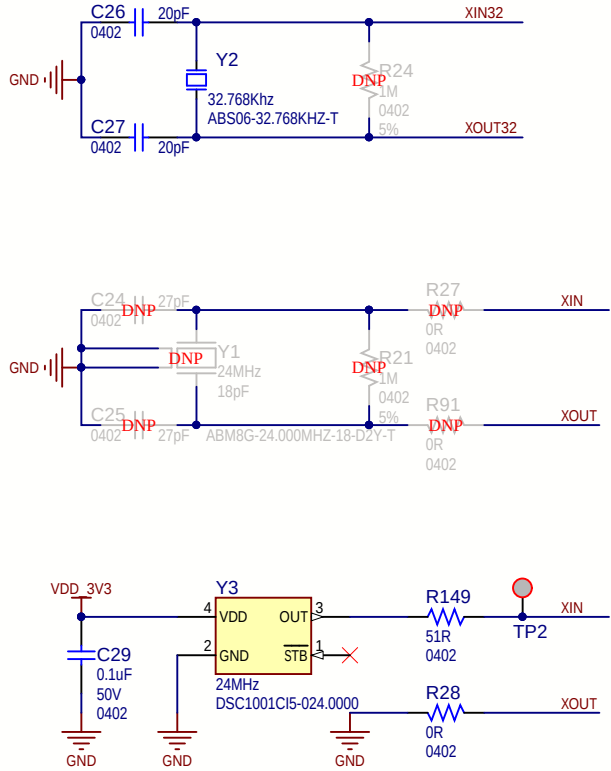


Decoupling caps should be placed as close as possible to their corresponding power balls
Place C19 close to L11 or P13, and place C30 close to P10

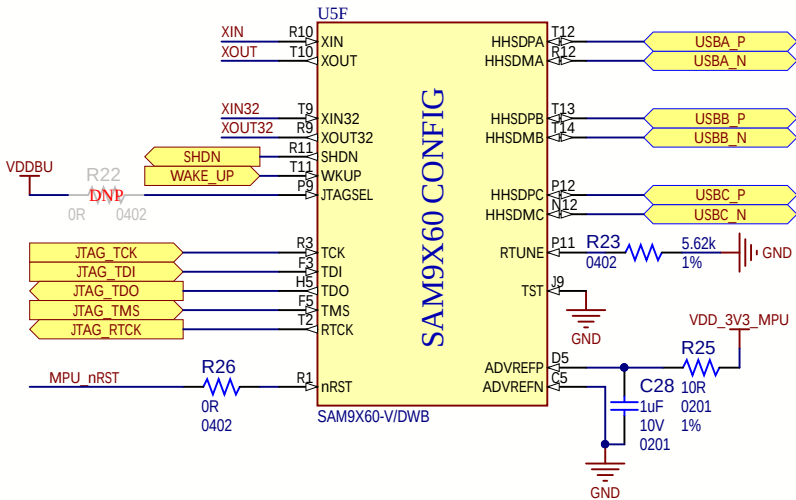
SAM9X60 DDR Controller



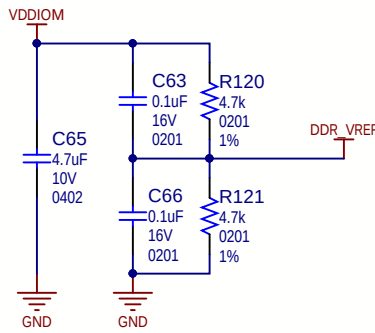
SAM9X60 Clocks



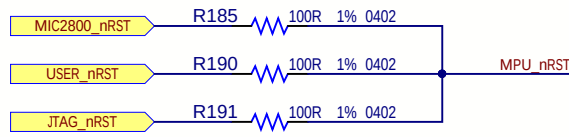
SAM9X60 Core



DDR_VREF



SAM9X60 Reset



Drawn By: IGN		 MICROCHIP	
Engineer: IGN			
PartNumber: DT100126		Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title Processor SAM9X60		Variant Name Standard_WILC3000_DNP	
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:44 AM	
	Revision:2	Sheet 3 of 13	
File: 03 - Processor SAM9X60.SchDoc			

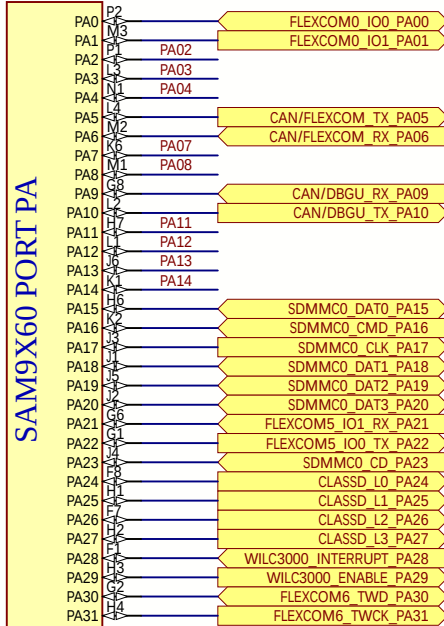
Designed with



Altium.com

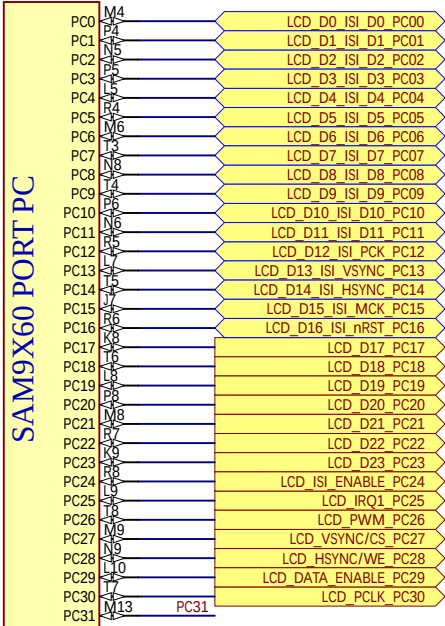
SAM9X60 PIOs

USA



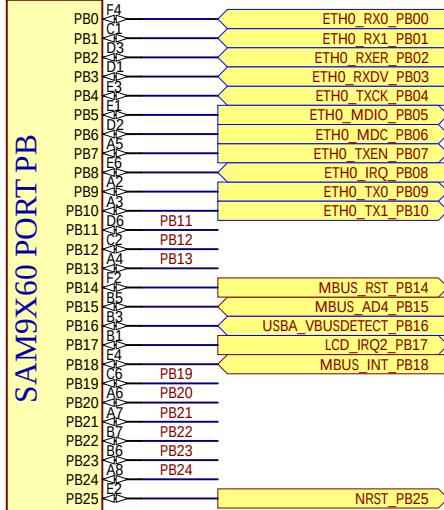
SAM9X60-V/DWB

U5C



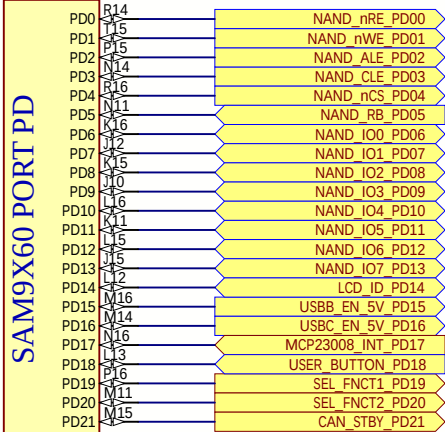
SAM9X60-V/DWB

USB



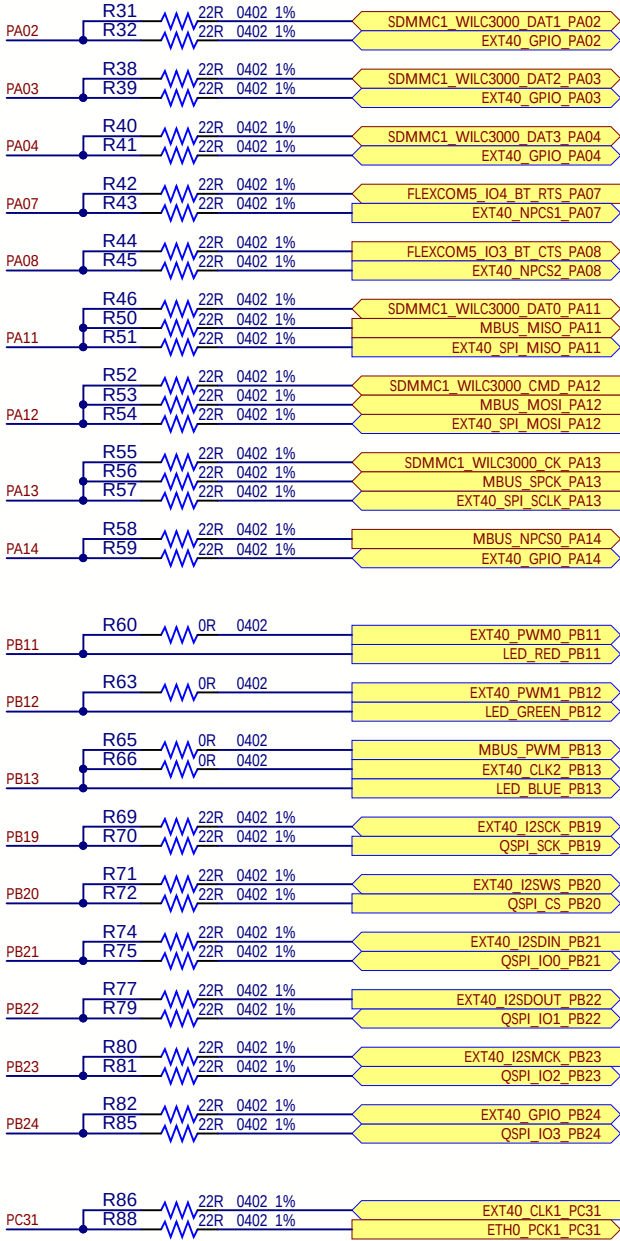
SAM9X60-V/DWB

U5D



SAM9X60-V/DWB

SAM9X60 PIO Muxing



Drawn By:
IGN

Engineer:
IGN

PartNumber:
DT100126

Sheet Title
Processor IOs

Size B

Sch #: 03-11039

Revision:2

File: 04 - Processor IOs.SchDoc



Project Title
SAM9X60 Evaluation Kit

Variant Name
Standard_WILC3000_DNP

Date: 9/20/2019 10:27:45 AM

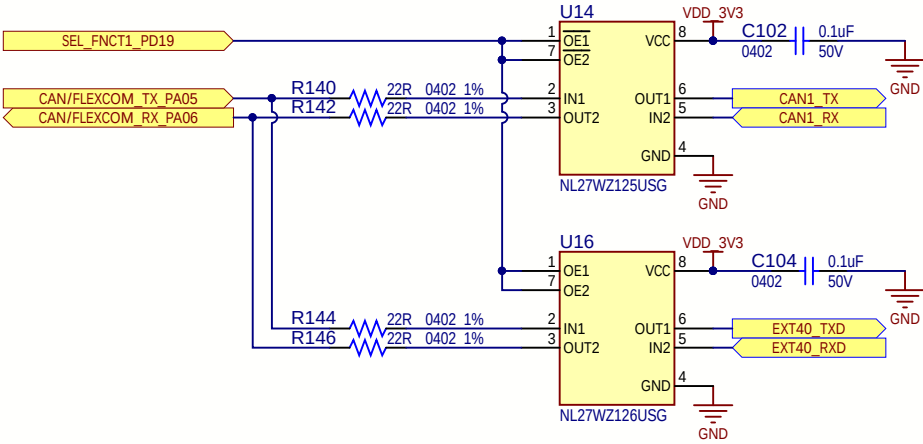
Sheet 4 of 13

Designed with

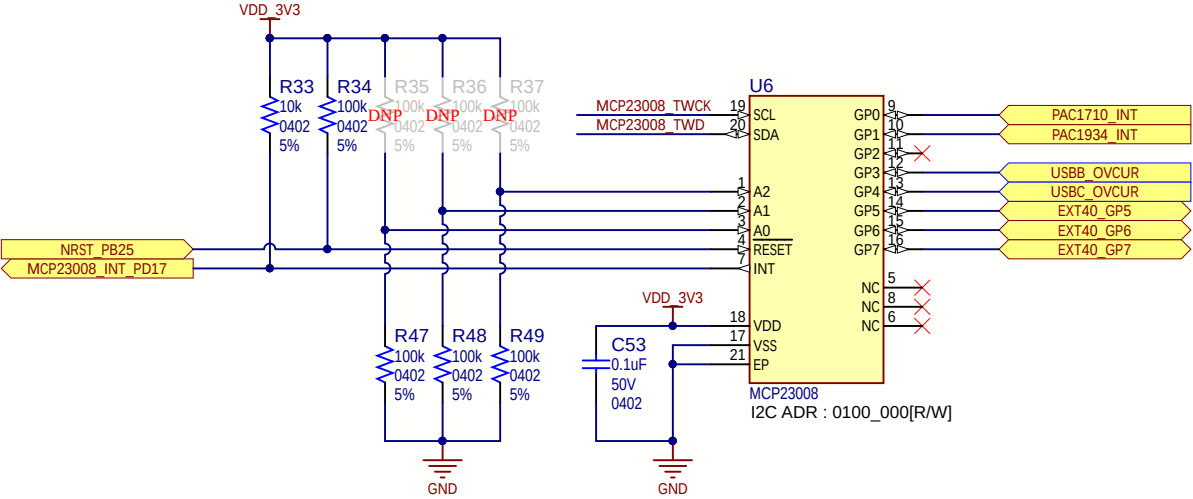


Altium.com

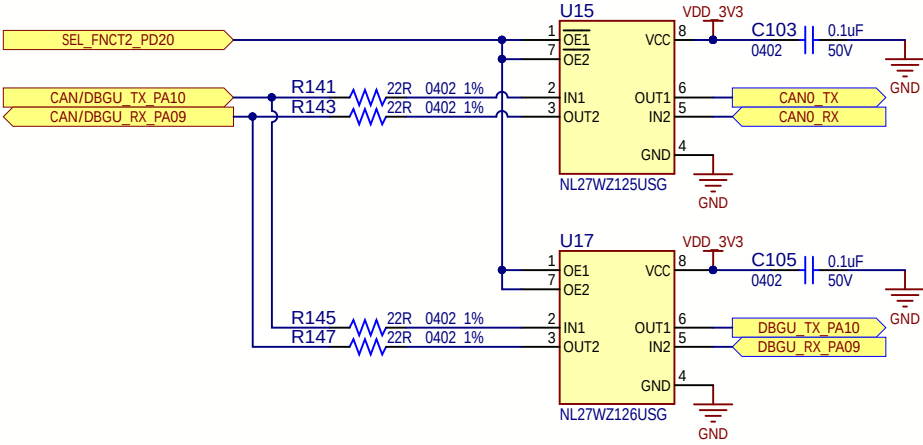
CAN1 / EXT40 UART Selection



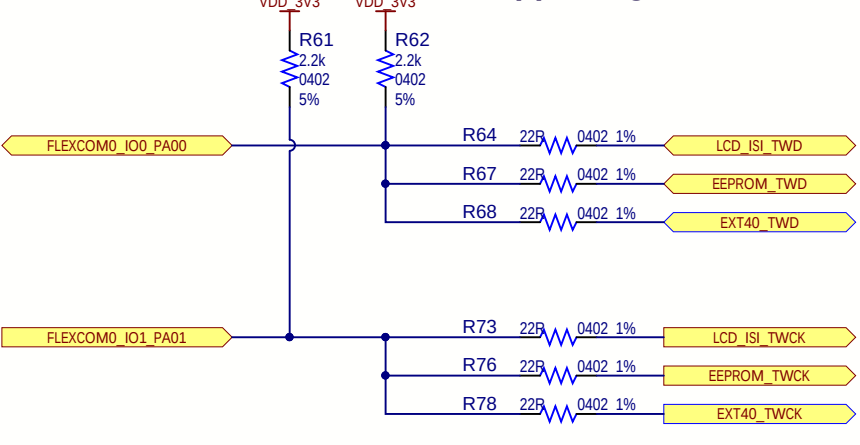
Port Expander



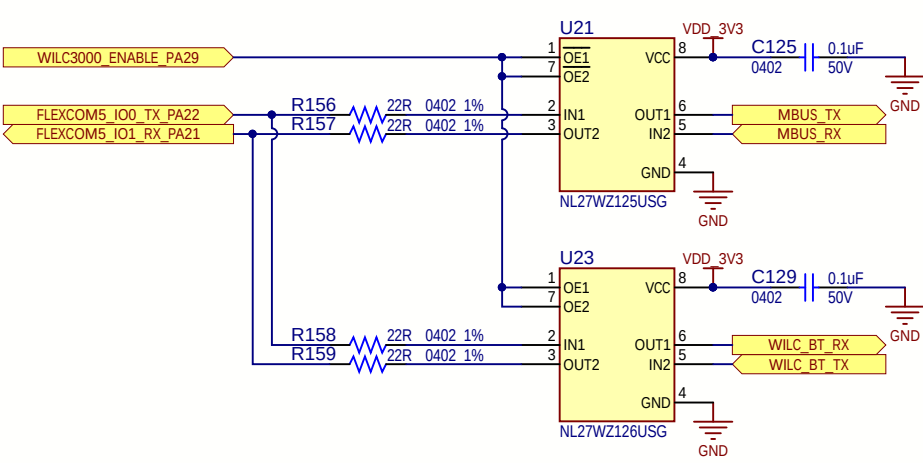
CAN0 / DEBUG UART Selection



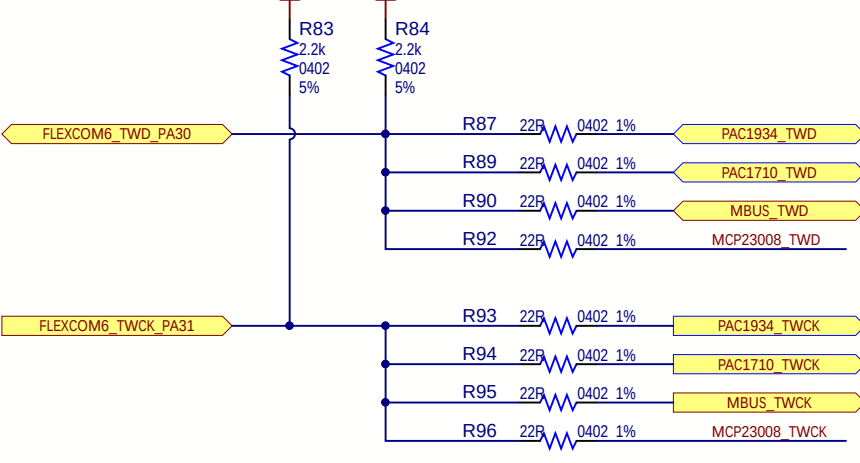
Upper Right Board TWI



MikroBUS UART / WILC BT UART Selection

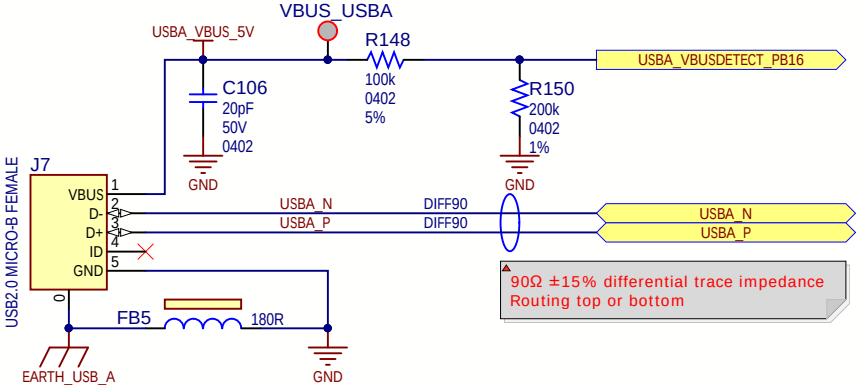


Lower Left Board TWI

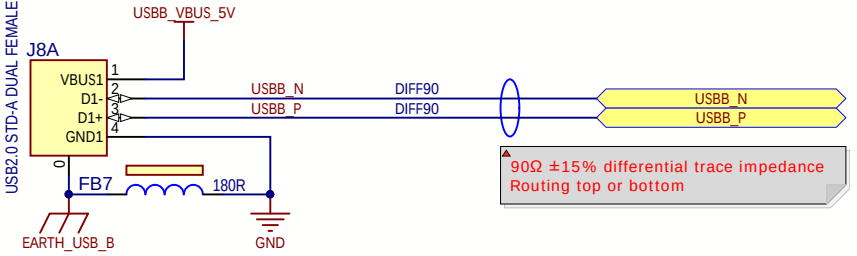


Drawn By: IGN		
Engineer: IGN		
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit	
Sheet Title Processor IO Expansions	Variant Name Standard_WILC3000_DNP	Designed with 
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:45 AM
	Revision: 2	Sheet 5 of 13
File: 05 - Processor IO Expansions.SchDoc		

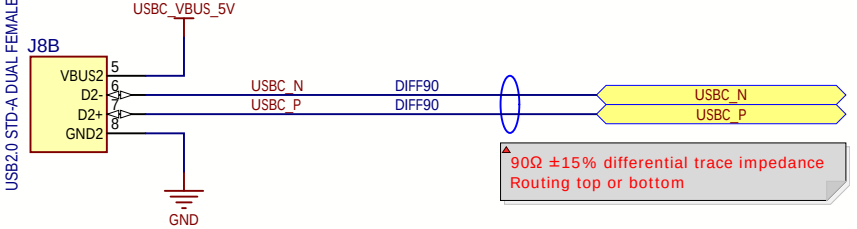
USB-A port



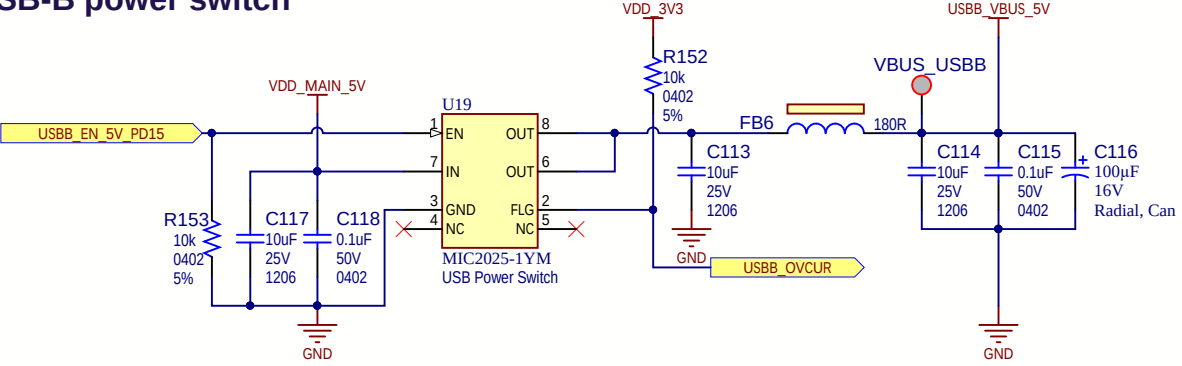
USB-B port



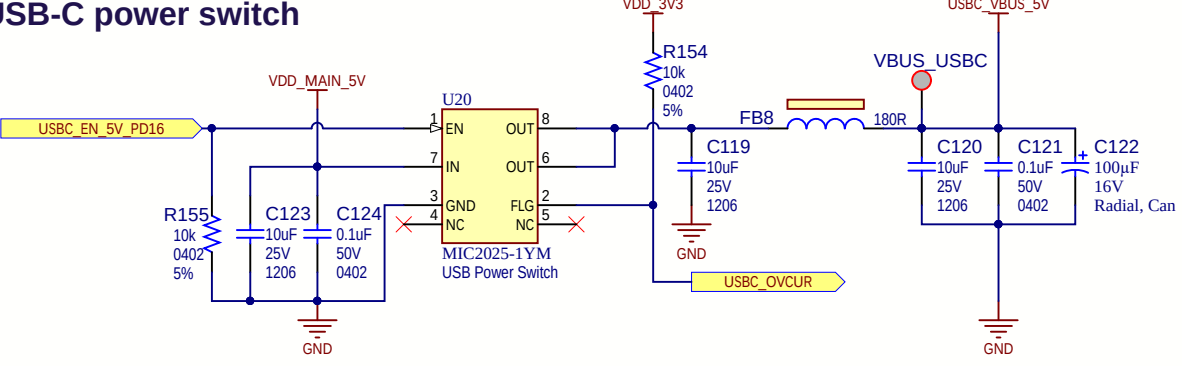
USB-C port



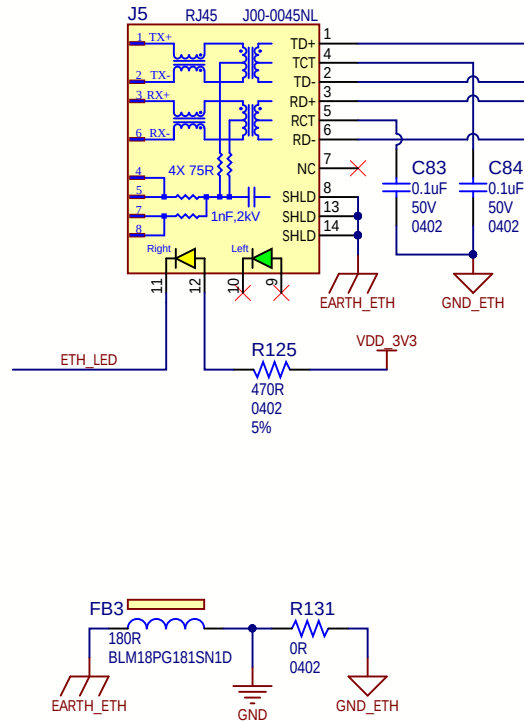
USB-B power switch



USB-C power switch

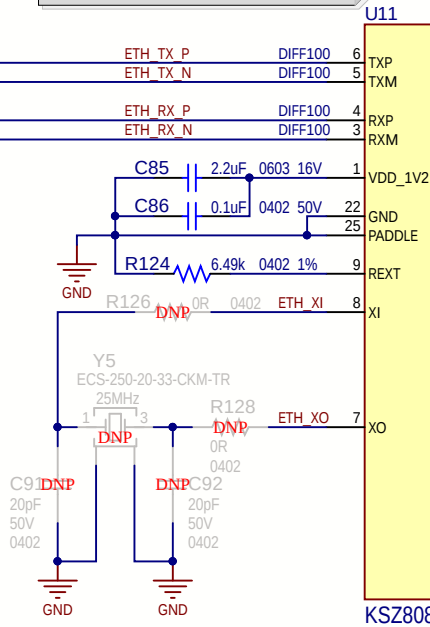
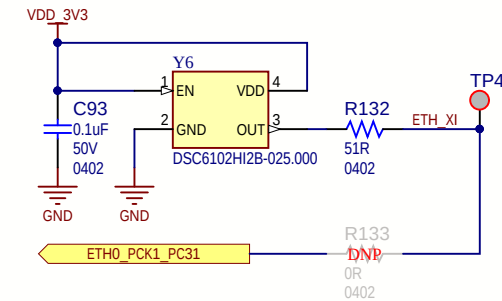


Ethernet Connector



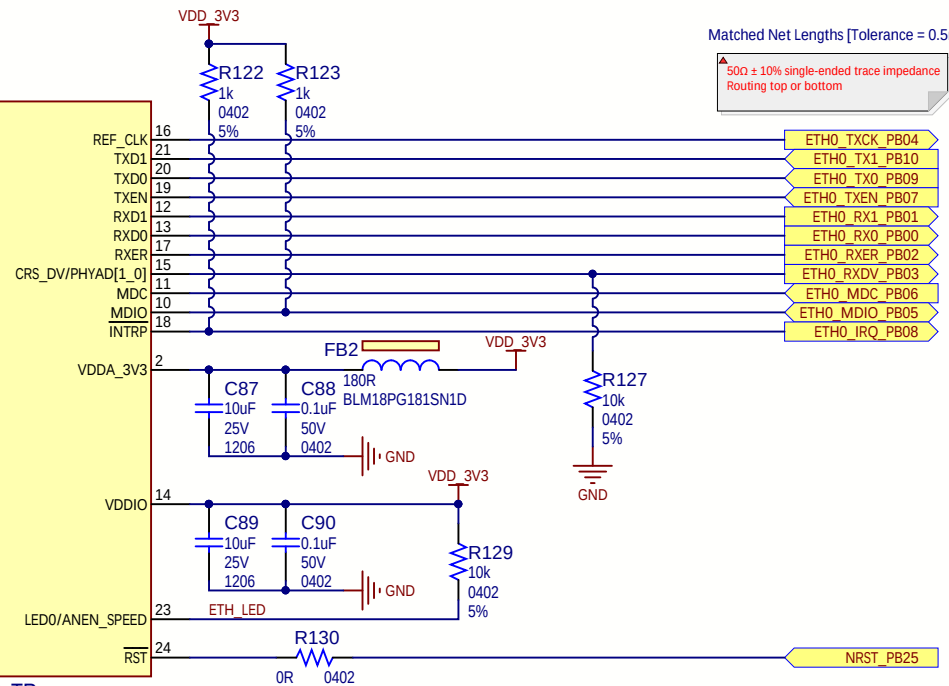
Ethernet PHY

▲ 100Ω ±5Ω differential trace impedance
Routing top or bottom



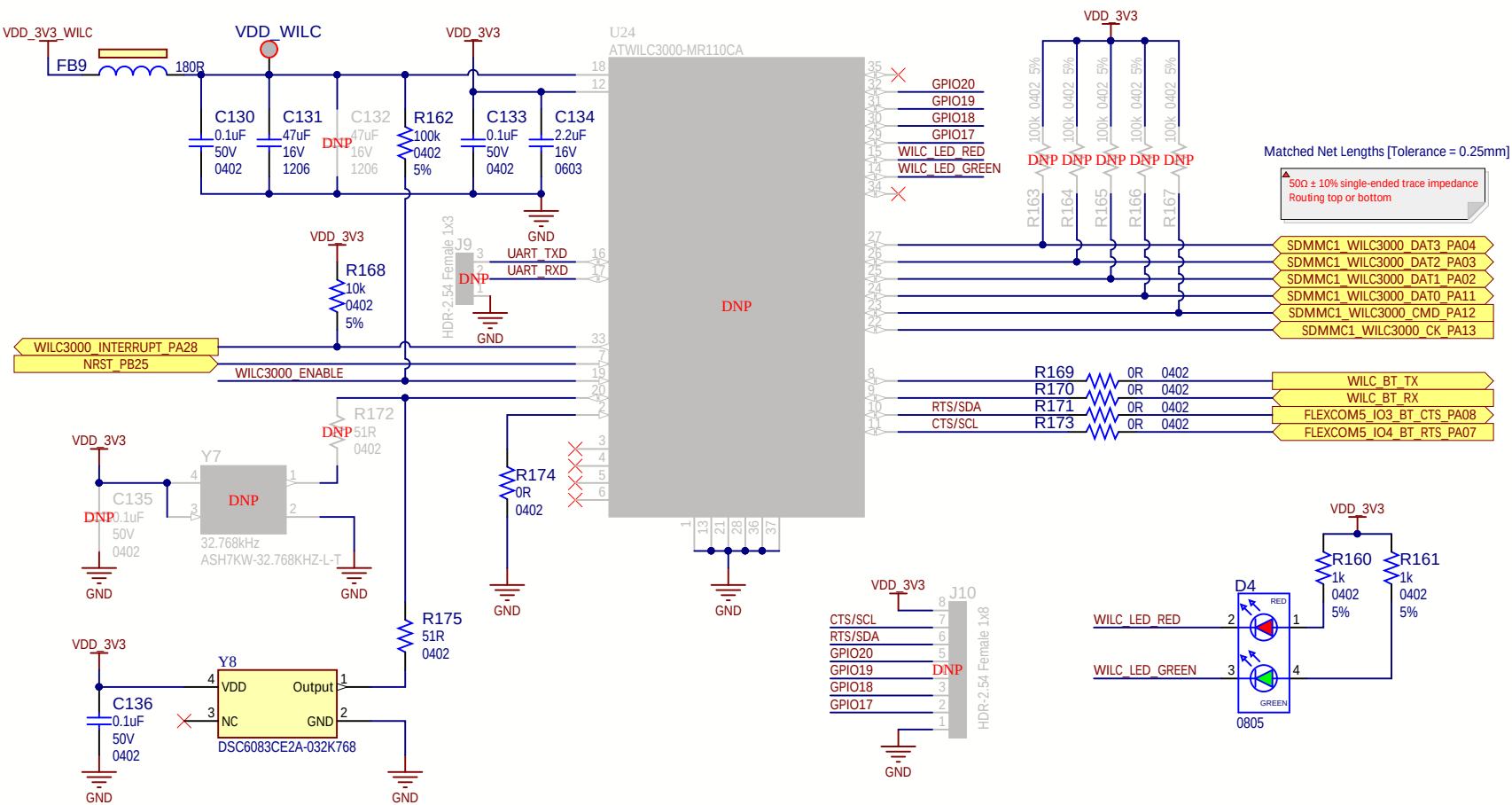
Matched Net Lengths [Tolerance = 0.5mm]

▲ 500 ±10% single-ended trace impedance
Routing top or bottom

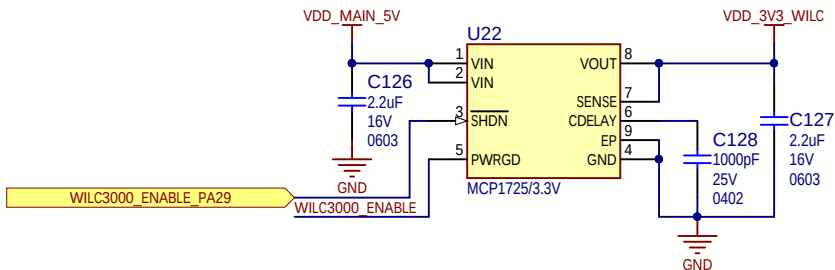


Drawn By: lGn			
Engineer: lGn			
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title Ethernet MAC	Variant Name Standard_WILC3000_DNP	Designed with 	
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:46 AM	Altium.com
Revision: 2	Sheet 8 of 13	File: 08 - Ethernet MAC.SchDoc	

WiFi / BT MODULE

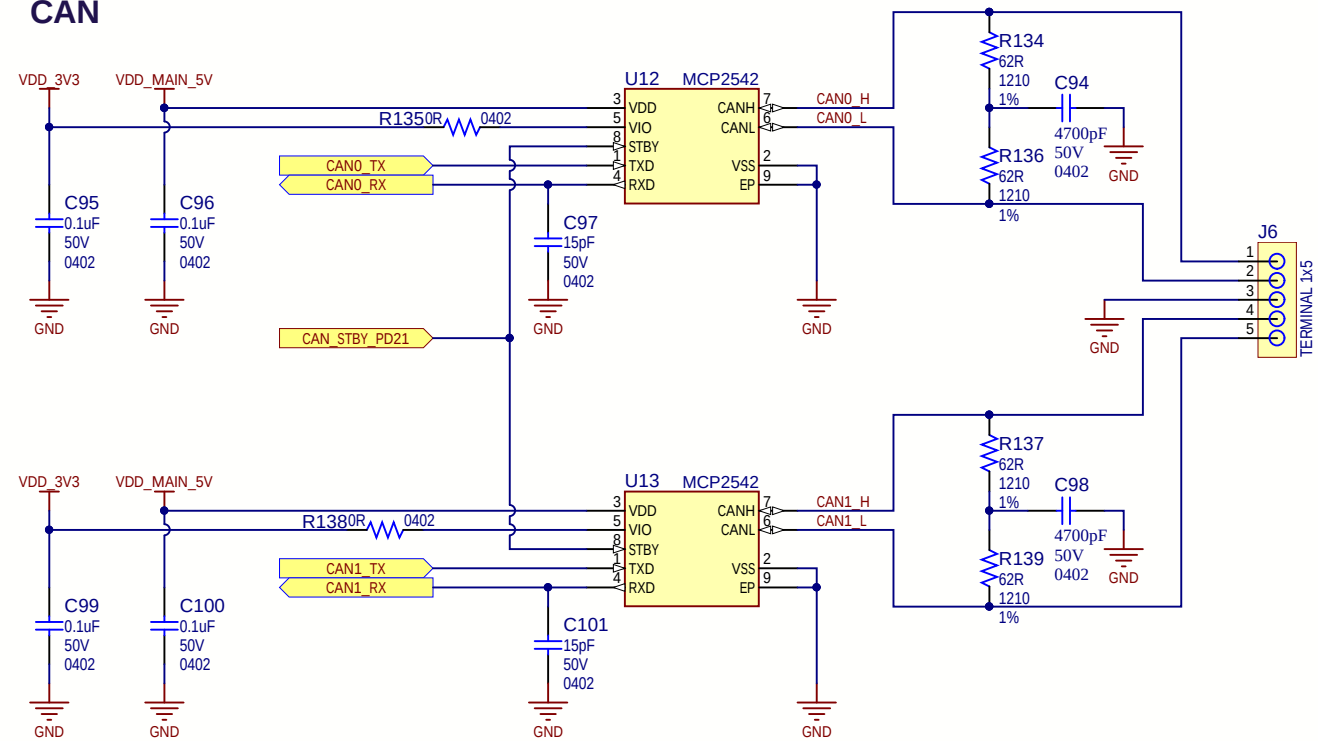


WiFi / BT ENABLE

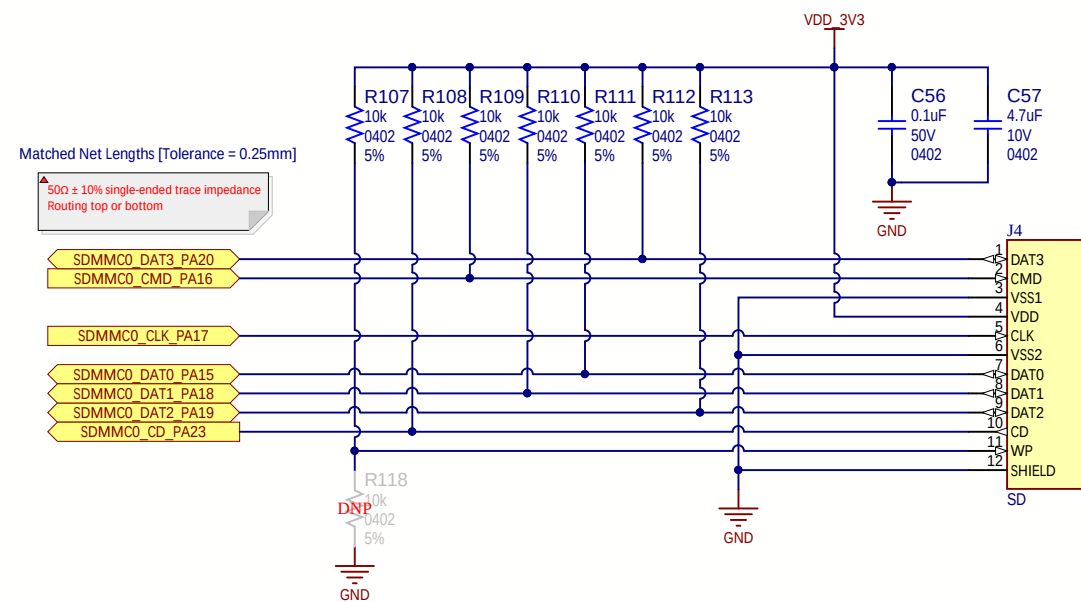


Drawn By: IGn			
Engineer: IGn			
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title Wi-Fi BT	Variant Name Standard_WILC3000_DNP	Designed with 	
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:46 AM	
	Revision:2	Sheet 9 of 13	
File: 09 - Wi-Fi BT module.SchDoc			

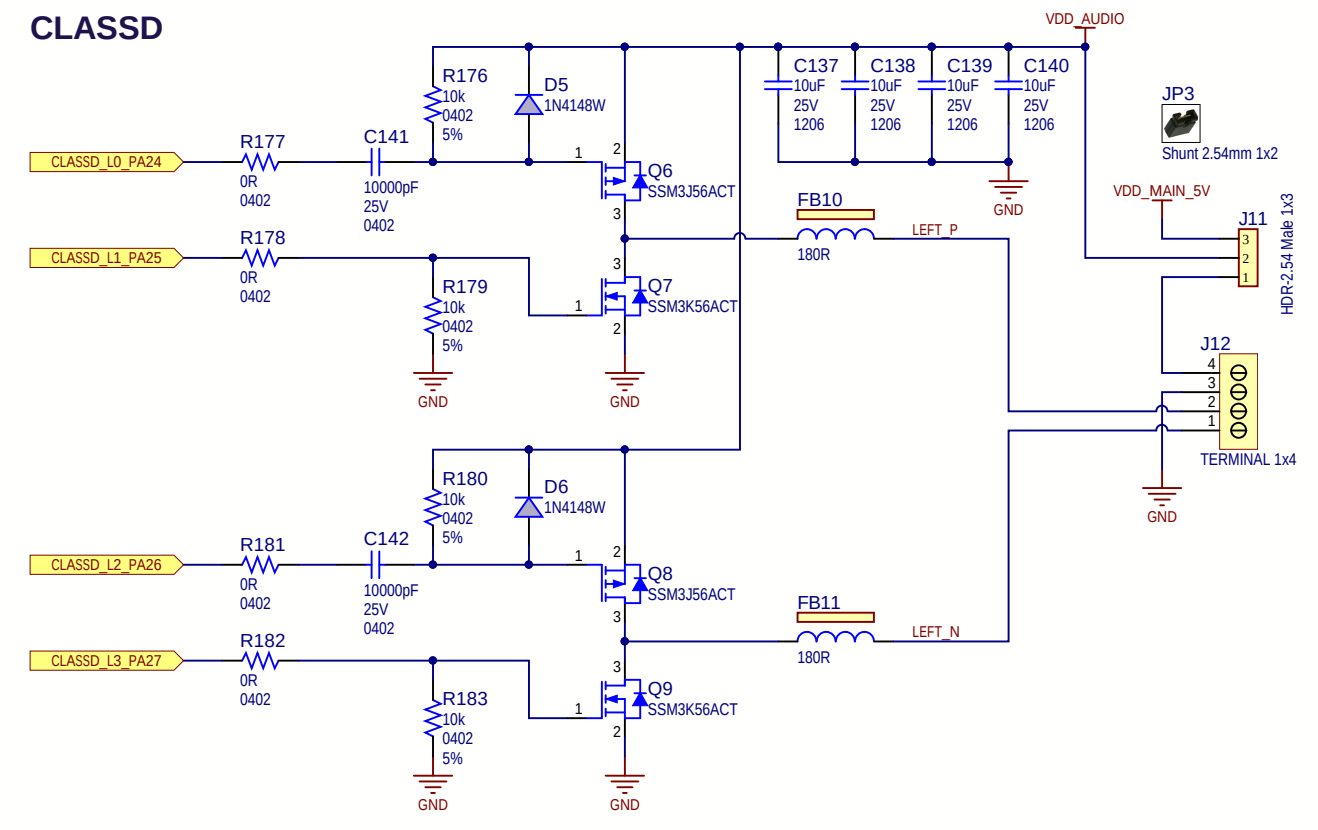
CAN



SDMMC

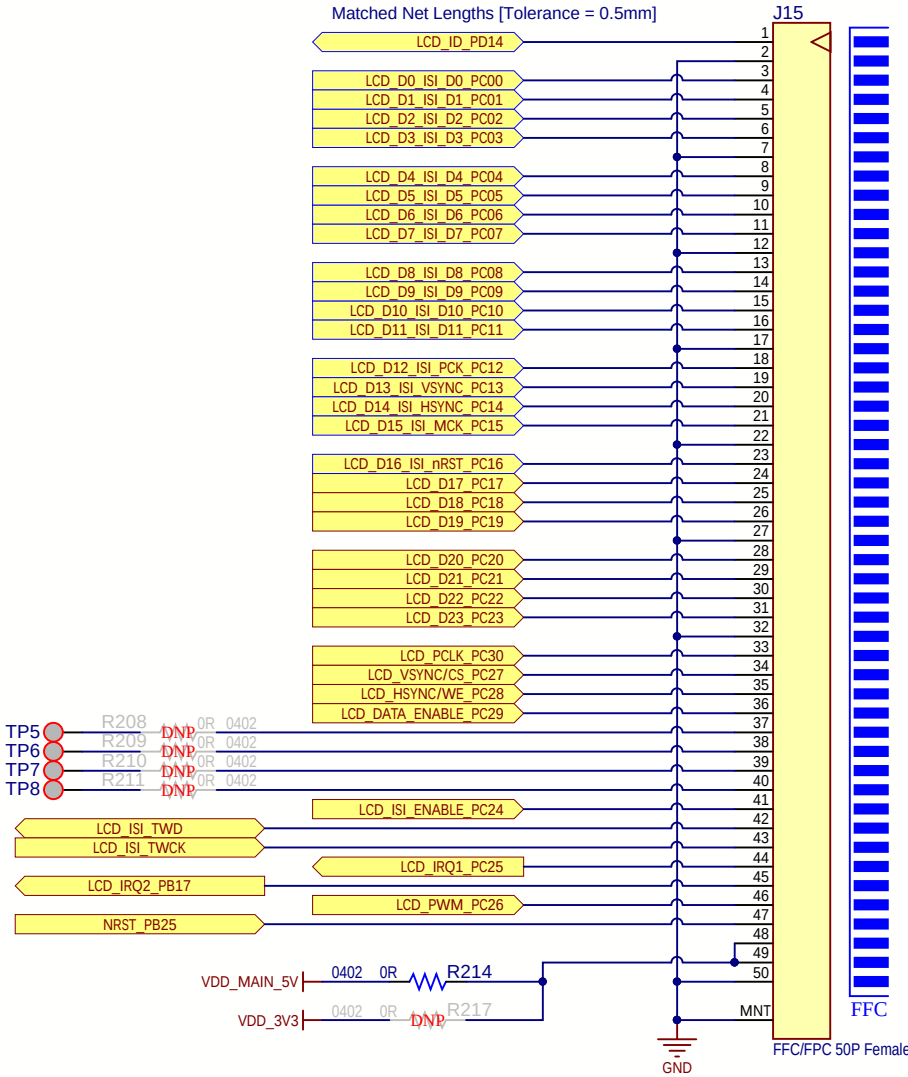


CLASSD

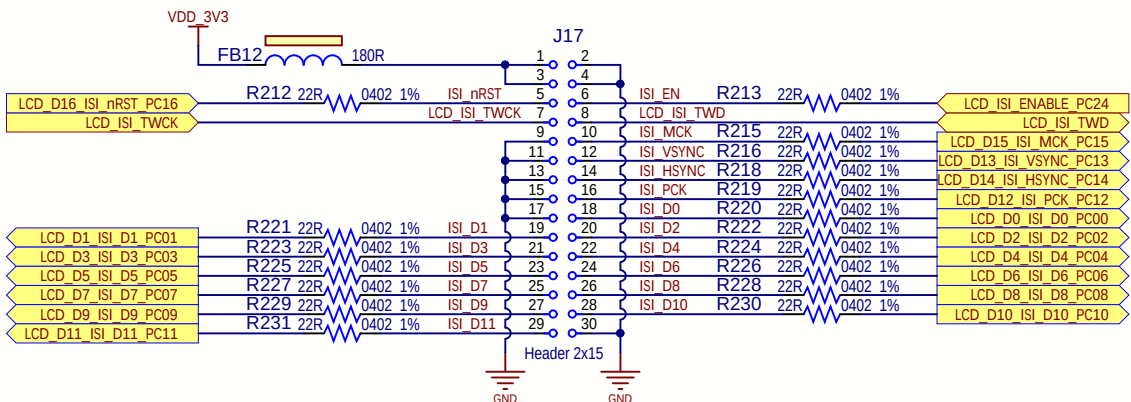


Drawn By: lGn	 MICROCHIP		
Engineer: lGn			
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title SDMMC, CAN & CLASSD	Variant Name Standard_WILC3000_DNP	Designed with  Altium	
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:46 AM	
	Revision:2	Sheet 10 of 13	
File: 10 - SDMMC, CAN & CLASSD.SchDoc			

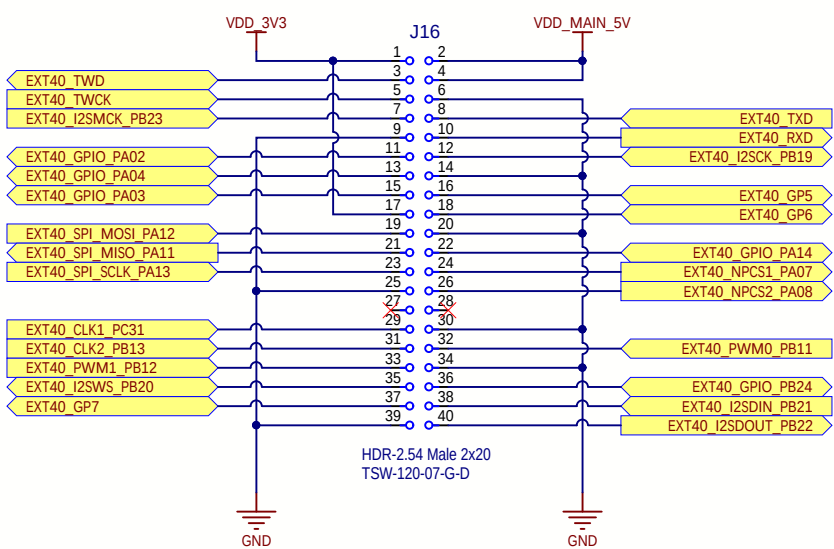
LCD



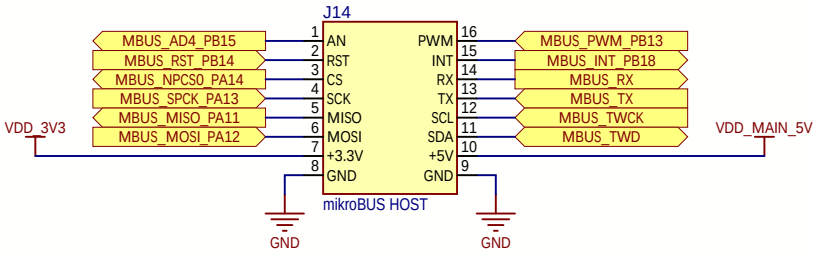
ISI Camera



40-pin GPIO

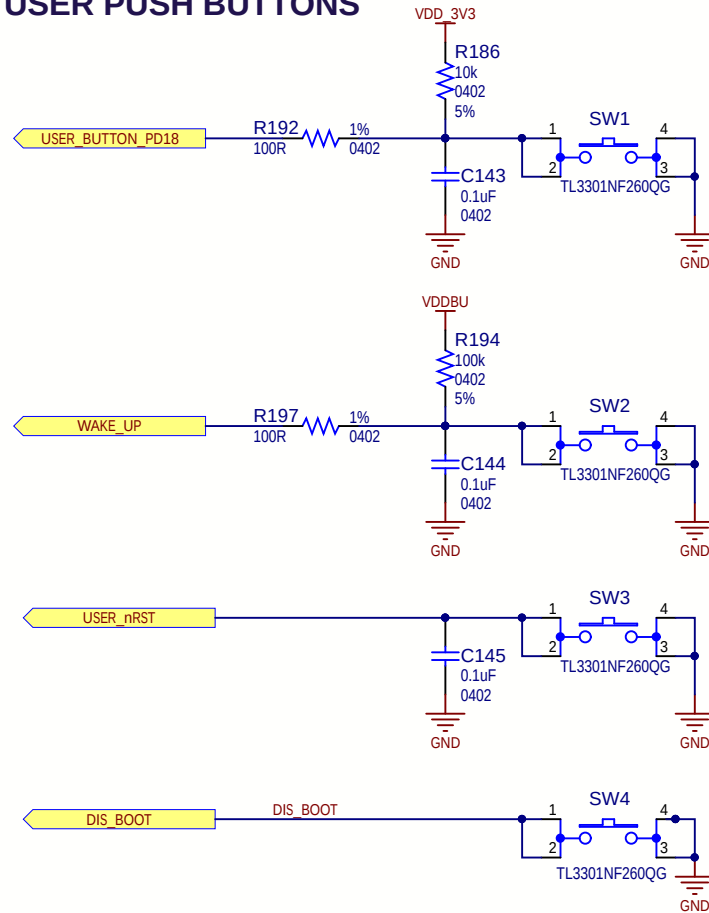


MikroBUS

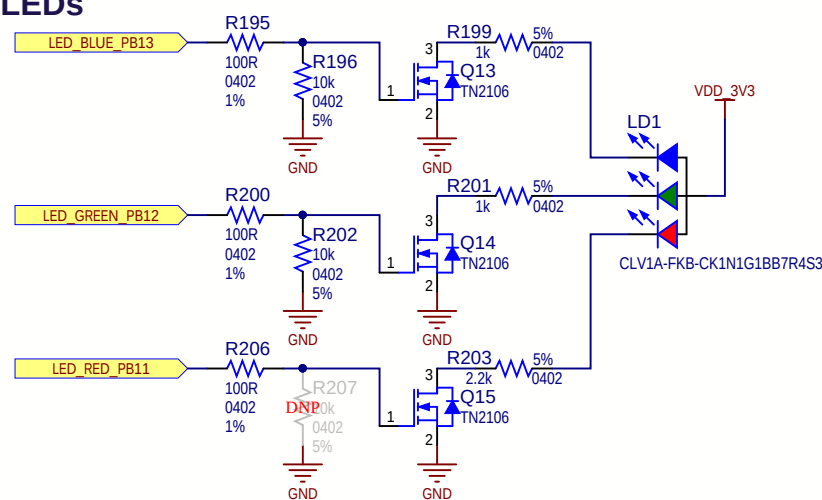


Drawn By: lGn	 MICROCHIP		
Engineer: lGn			
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title Expansion connectors	Variant Name Standard_WILC3000_DNP	Designed with  Altium	
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:47 AM	Revision: 2
File: 11 - Expansion connectors.SchDoc		Sheet 11 of 13	

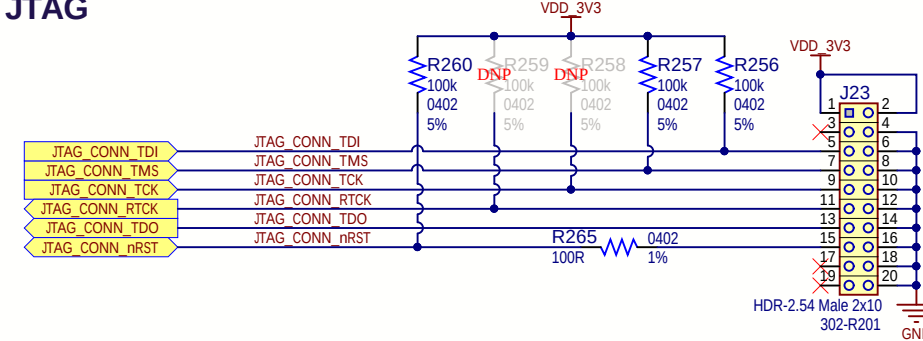
USER PUSH BUTTONS



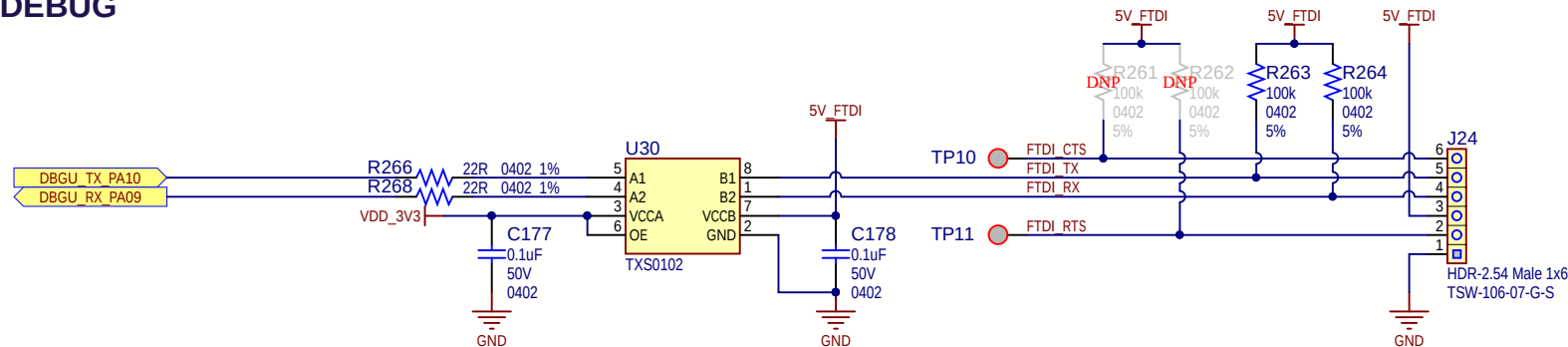
RGB LEDs



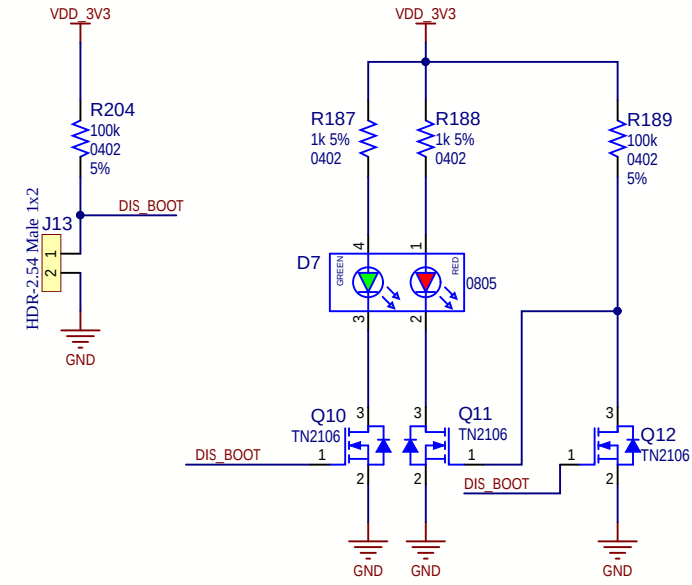
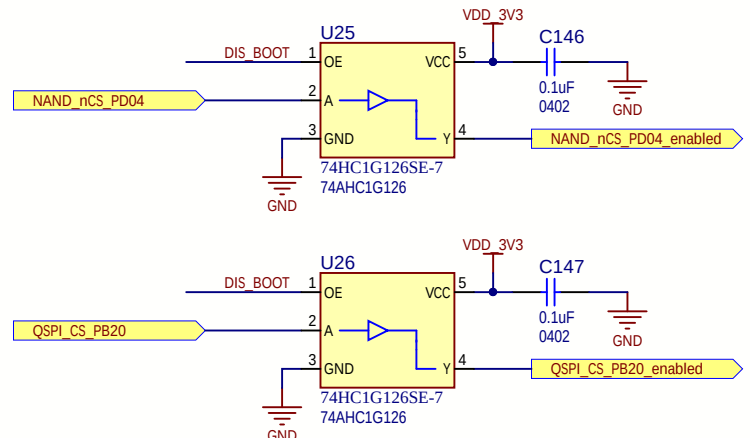
JTAG



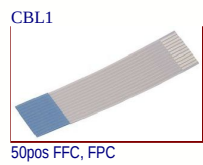
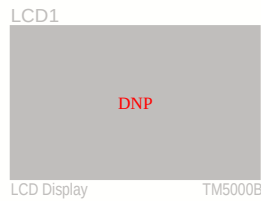
FTDI DEBUG



DISABLE BOOT

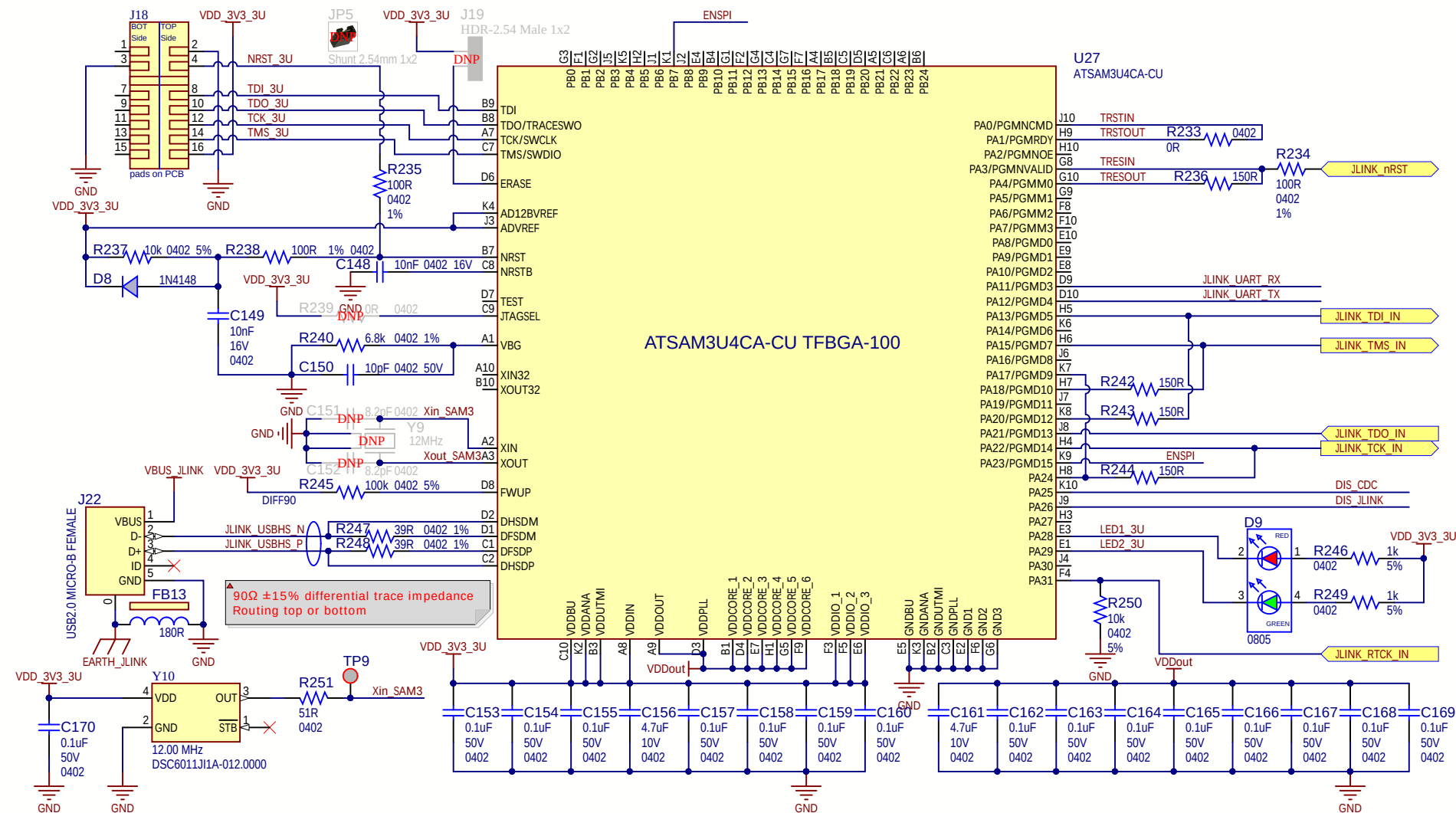


PLACEHOLDERS

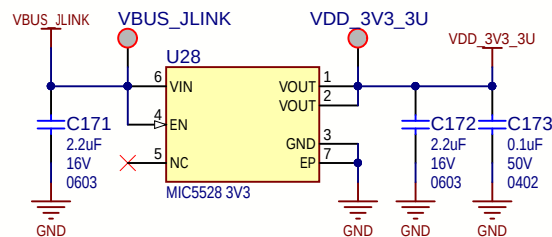


Drawn By: IGn		
Engineer: IGn	Project Title SAM9X60 Evaluation Kit	
PartNumber: DT100126	Variant Name Standard_WILC3000_DNP	
Sheet Title User Interaction	Variant Name Standard_WILC3000_DNP	Designed with
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:47 AM
	Revision:2	Sheet 12 of 13
File: 12 - User Interaction.SchDoc		

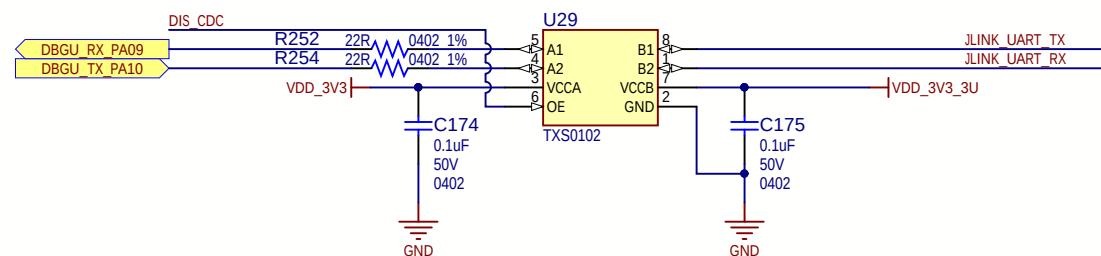
JLINK-OB INTERFACE



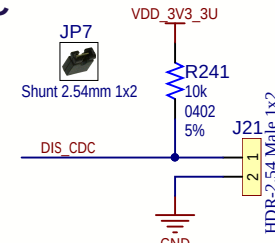
JLINK-OB POWER SUPPLY



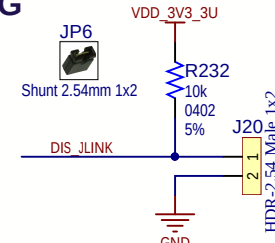
LEVEL SHIFTING UART - JLINK-OB



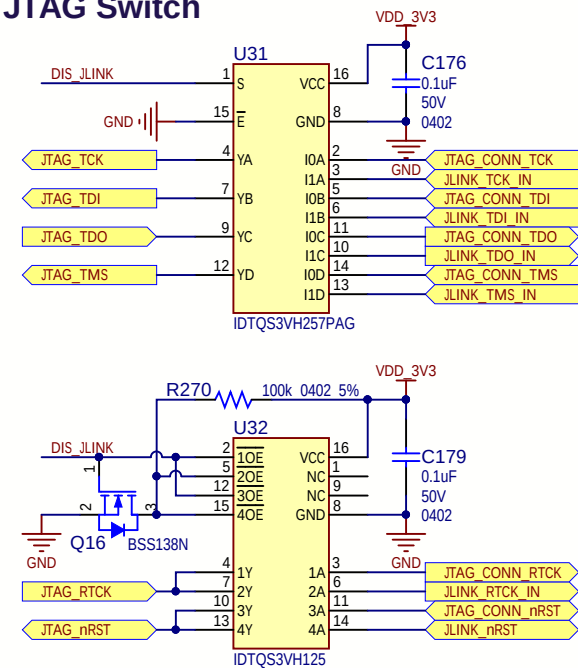
DIS JLINK CDC



DIS JLINK JTAG



JTAG Switch



Drawn By:
IGN

Engineer:
IGN

PartNumber:
DT100126

Sheet Title
On Board JLINK

Size B

File: 13 - On Board JLINK.SchDoc



Project Title
SAM9X60 Evaluation Kit

Variant Name
Standard_WILC3000_DNP

Date: 9/20/2019 10:27:47 AM

Sheet 13 of 13

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